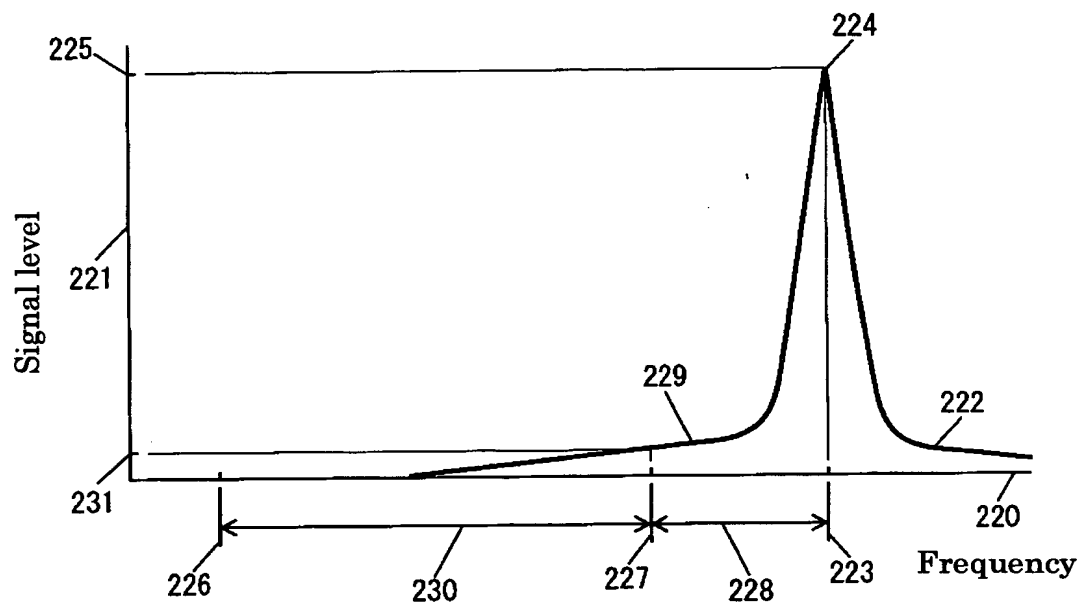


FIG. 2



F I G . 4

Receiving channel	Phase shifter control voltage	PLL data	Level regulator
1 CH	.	.	.
2 CH	.	.	.
.	.	.	.
.	.	.	.
.	.	.	.
.	.	.	.
.	.	.	.
.	.	.	.
CH	.	.	.
.	.	.	.
.	.	.	.
.	.	.	.
.	.	.	.
CH	.	.	.

FIG. 5

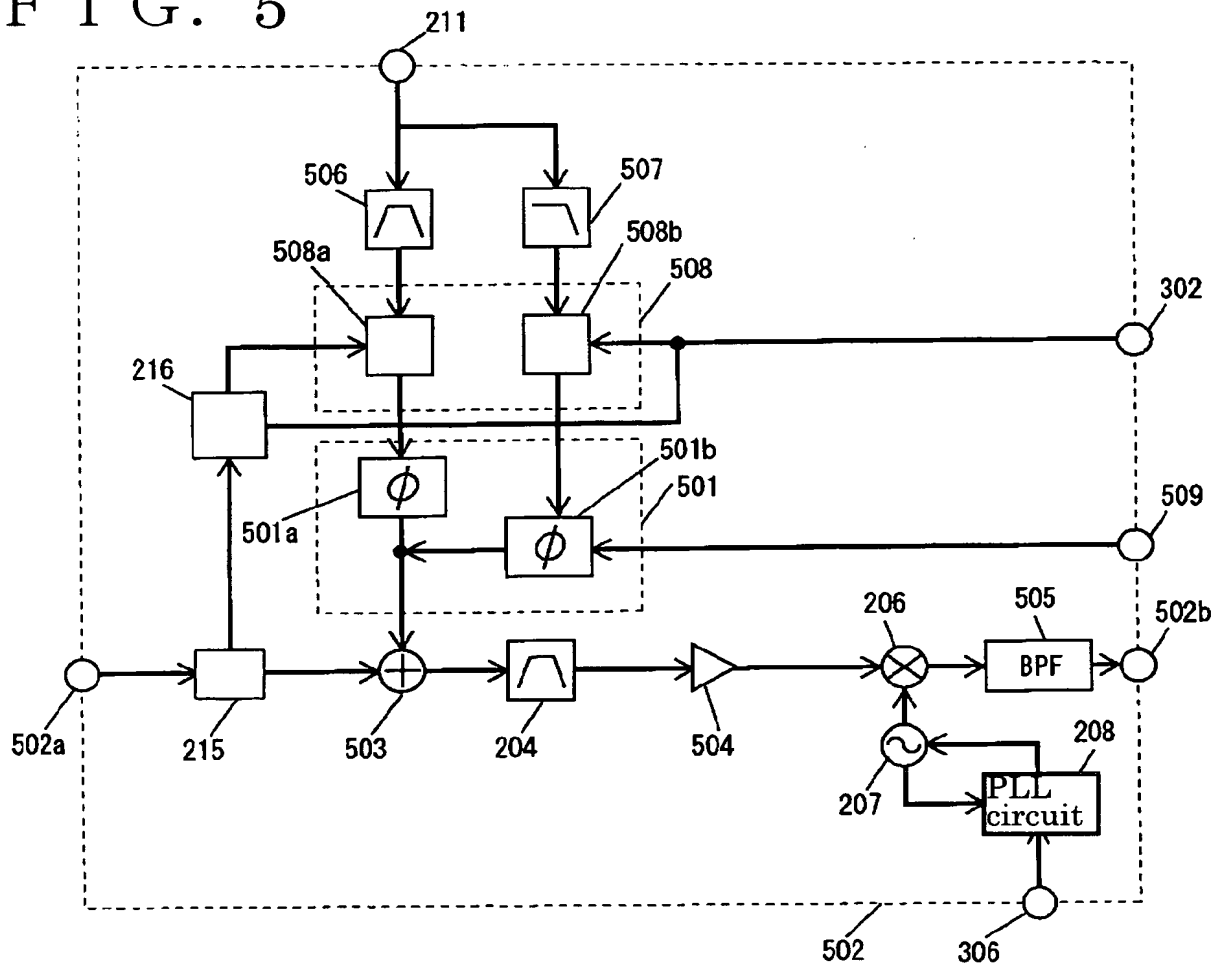


FIG. 6

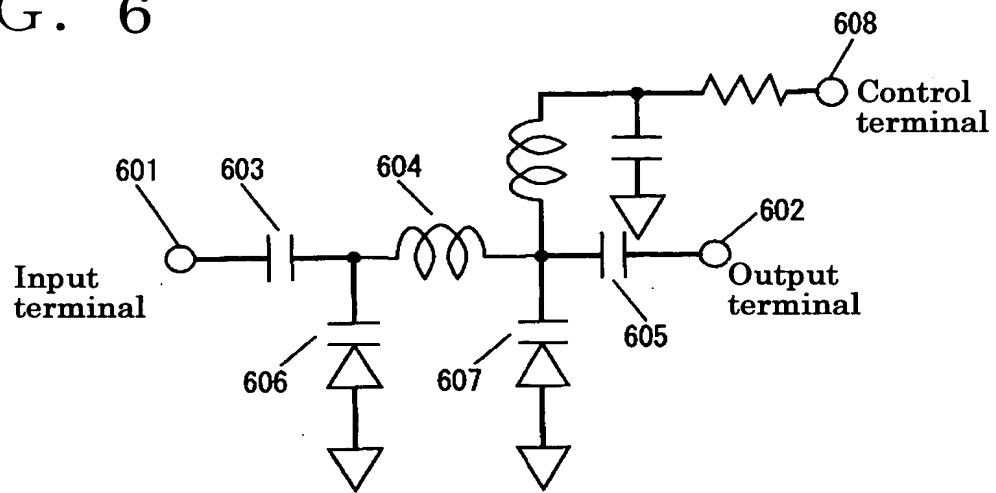


FIG. 7

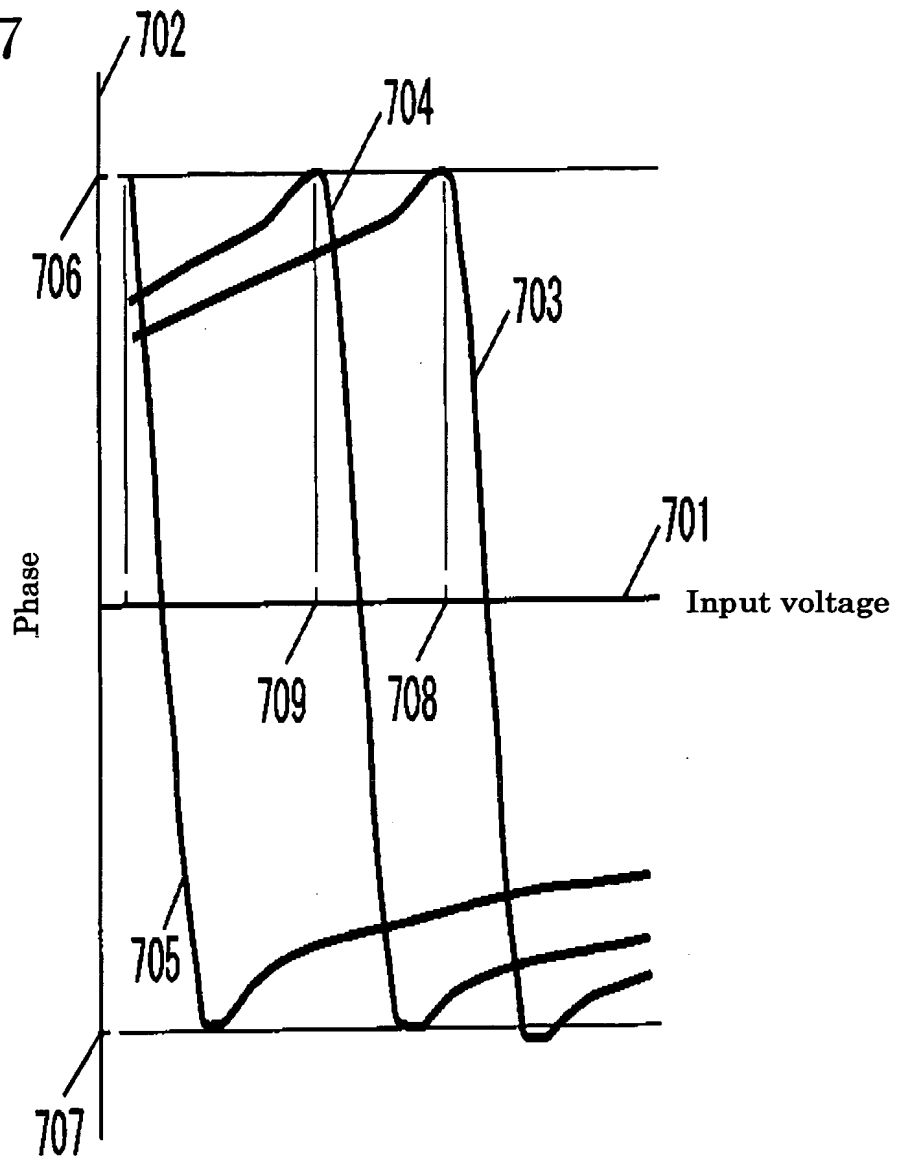


FIG. 8

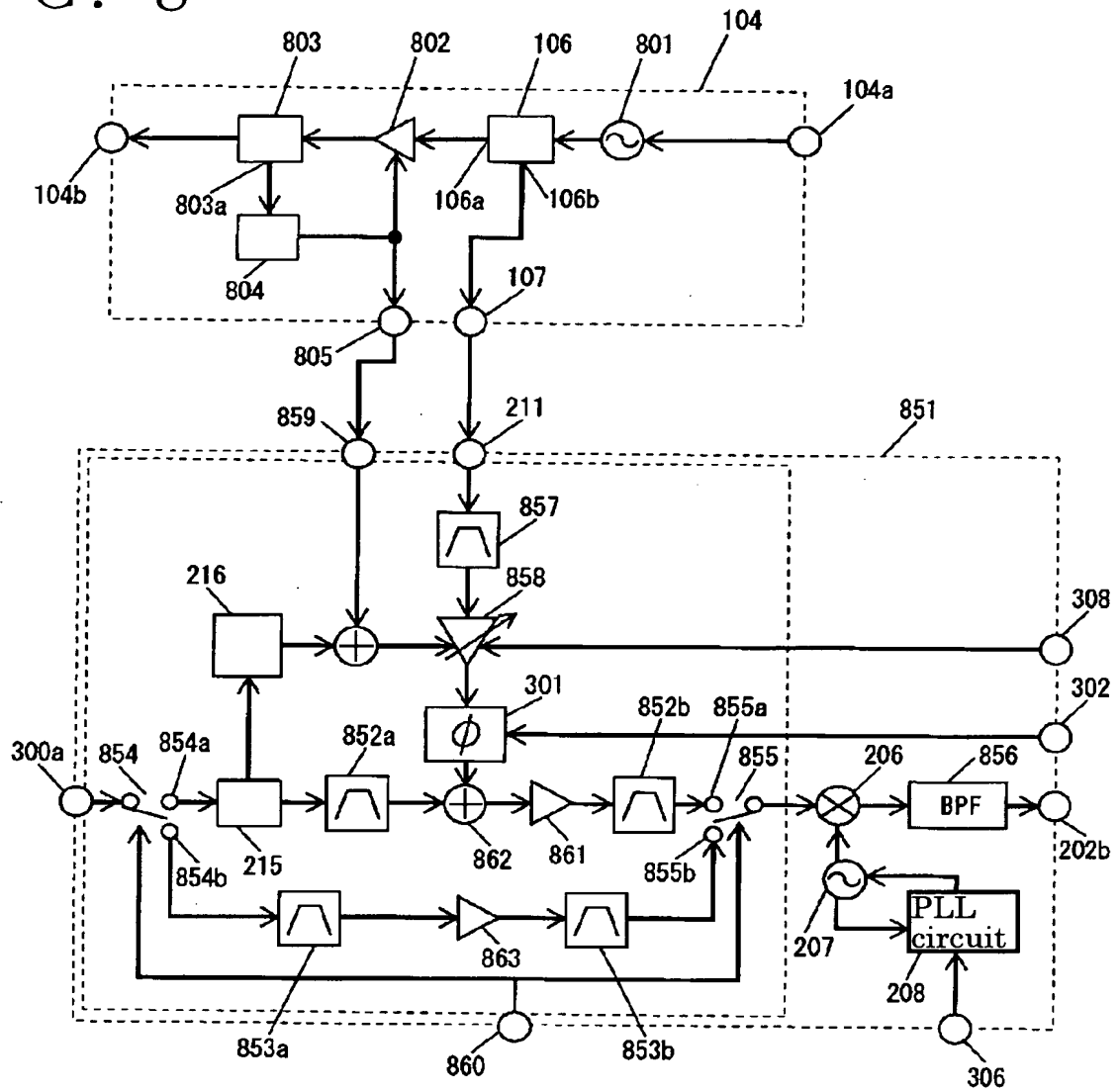


FIG. 9

Receiving channel	Phase shifter Control voltage	Switch	PLL data	AGC	Amplifier	Amplifier
1 CH	•	•	•	•	•	•
2 CH	•	•	•	•	•	•
•	•	•	•	•	•	•
•	•	•	•	•	•	•
•	•	•	•	•	•	•
•	•	•	•	•	•	•
•	•	•	•	•	•	•
•	•	•	•	•	•	•
CH	•	•	•	•	•	•
•	•	•	•	•	•	•
•	•	•	•	•	•	•
•	•	•	•	•	•	•
CH	•	•	•	•	•	•

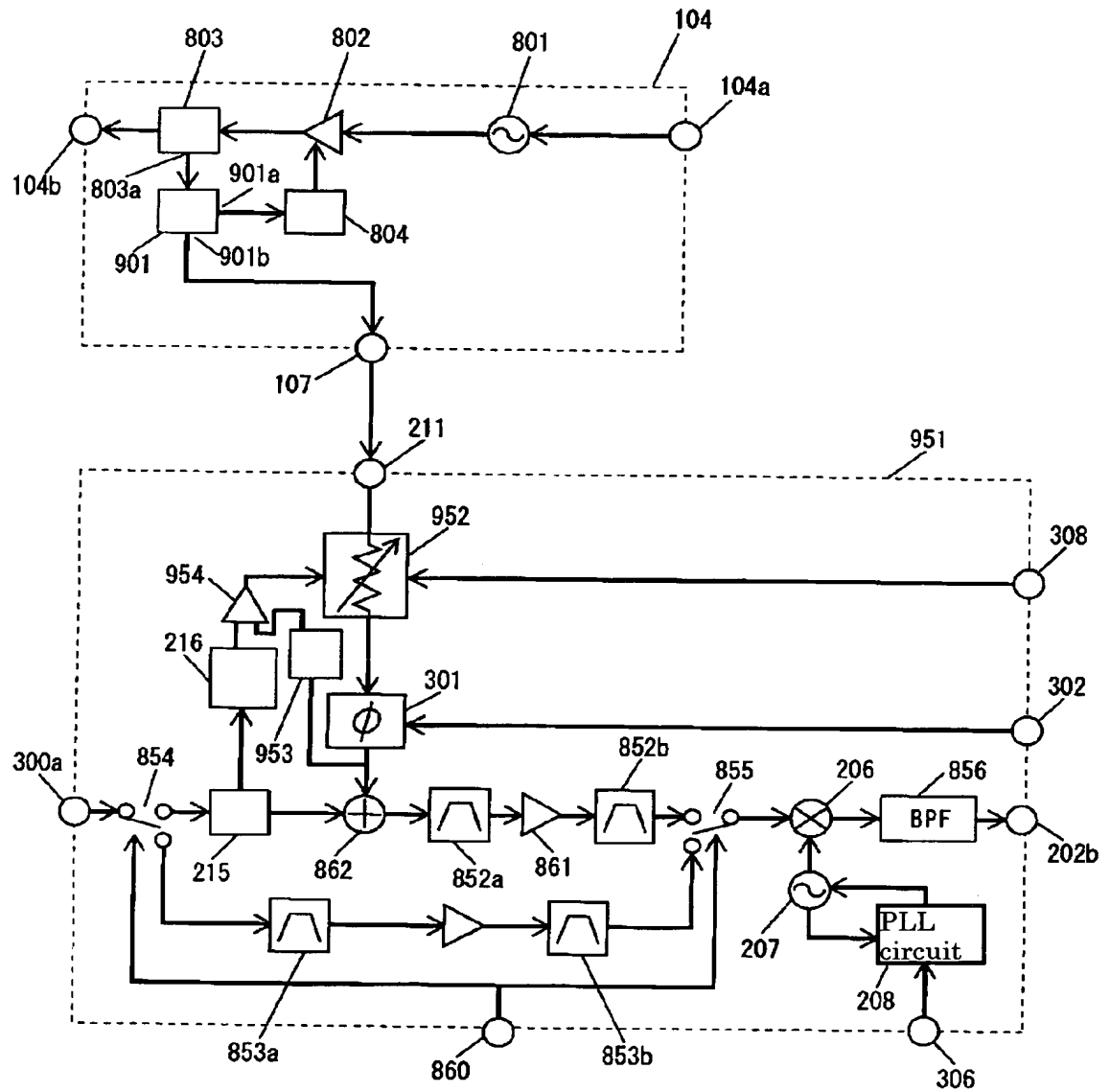


FIG. 11

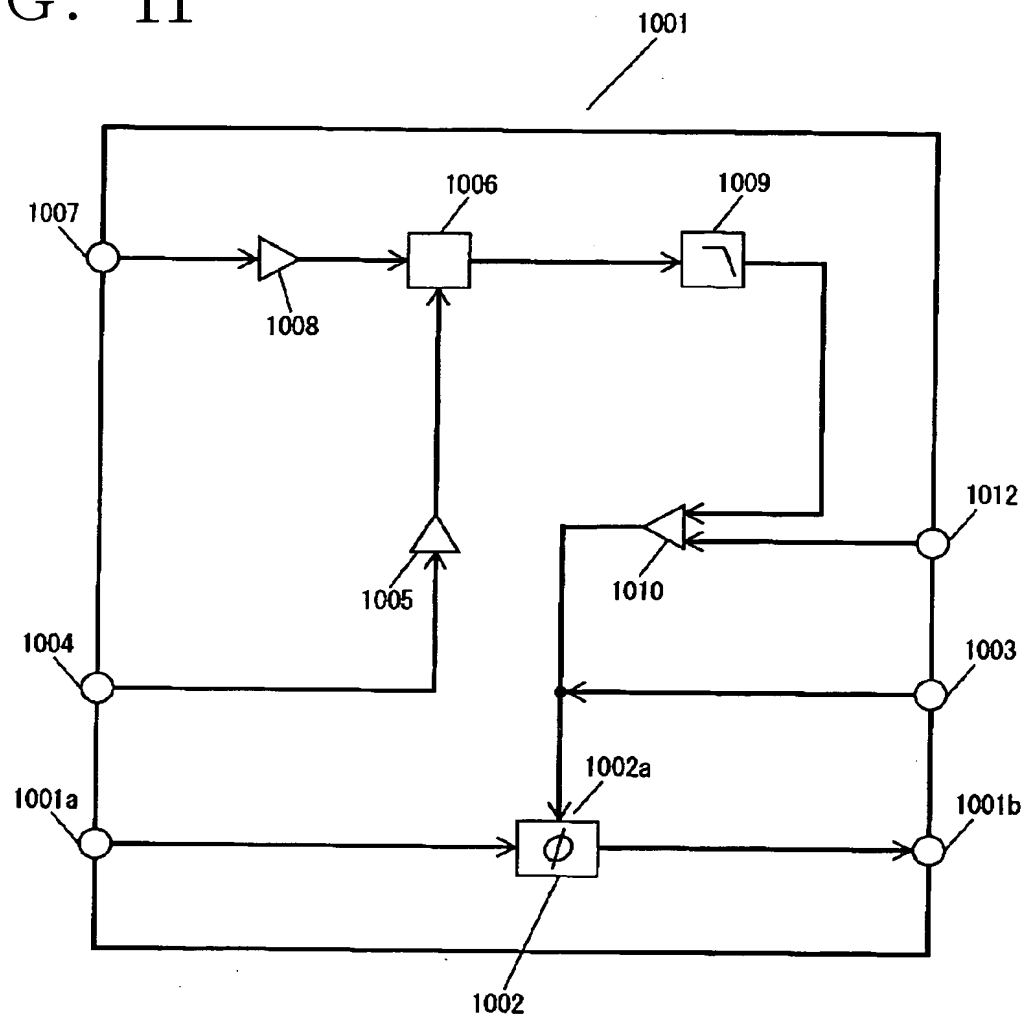


FIG. 12

